



PATENT
P56833

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: HYONG-KYUN LEE

Serial No.: 10/607,167

Examiner: *To be assigned*

Filed: 27 June 2003

Art Unit: 2121

For: METHOD FOR COMMONLY CONTROLLING DEVICE DRIVERS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §1.56, and §§1.97 and 1.98 as amended, Applicant cites, describes, and provides copies of the following art references:

FOREIGN PATENT REFERENCE:

- Japanese Patent Publication No. 10-011384 to Tanaka, entitled *INPUT/OUTPUT STANDARDIZATION DEVICE* published on 16 January 1998 (with English abstract).

OTHER DOCUMENTS:

- Japanese Office action for Japanese Patent Application No. 2003-289785, issued on 7 March 2006.

DISCUSSION

Tanaka JP'384, according to the Japanese Office action in applicant's Japanese patent application Serial No. 2003-289785, discloses that input/output standardization mechanism 10 to or from the peripheral devices is constituted of the tree layers of an application interface layer 30,

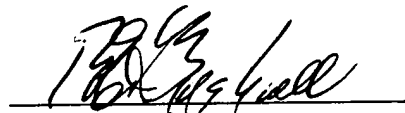
a logical channel ↔ physical channel converting layer 40 and a device driver interface layer 50. Then, the application program 1 permits the input/output to or from a peripheral device group to be possible without being conscious of difference in a communication board by a common communication function with a logical channel which is provided by the interface layer 30. Thus, when system configuration is changed, only the interface layer 50 is changed so that it corresponds to the change of system configuration without adding any program change to the application program 1.

Pursuant to 37 CFR §1.97(d), the undersigned attorney hereby certifies that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign patent application not more than three (3) months prior to the filing of the statement.

The citation of the foregoing references is not intended to constitute an assertion that other or more relevant art does not exist. Accordingly, the Examiner is requested to make a wide-ranging and thorough search of the relevant art.

No fee is incurred by this Statement.

Respectfully submitted,



Robert E. Bushnell
Reg. No.: 27,774
Attorney for the Applicant

1522 "K" Street, N.W., Suite 300
Washington, D.C. 20005
Area Code: (202) 408-9040
Folio: P56883 Date: 3/15/06 I.D.: REB/ks

**INFORMATION DISCLOSURE STATEMENT****PTO-1449 (PAGE 1 OF 1)****SERIAL NUMBER** 10/607,167**DOCKET NO.** P56833**APPLICANT** HYONG-KYUN LEE**FILING DATE** 27 June 2003**GROUP** 2121**U.S. PATENT DOCUMENTS**

EXAMINER	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS**TRANSLATION**

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO
	JP 10-011384	01/98	JAPAN			Abstract	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

Japanese Office Action of the Japanese Patent Application No. 2003-289785, mailed on 7 March 2006

EXAMINER:**DATE CONSIDERED:**

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP §609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.